IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. A. Fathimulla, et al.

Serial No.:

For: SILICON-ON-INSULATOR WAFER FOR RF INTEGRATED CIRCUIT

Filed:

Probable Group Art Unit: 2814

Probable Examiner: S. H. Rao

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450 on this date:

January 26, 2004

Trevor B. Jokke Reg. No. 25,542

Attorney for Applicants

PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTION

Claims 1-6 and 22-31 remain in the application. Claims 7-21 are cancelled.